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**SUPPLEMENTAL
FORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Substitute for form 1449A/PTO

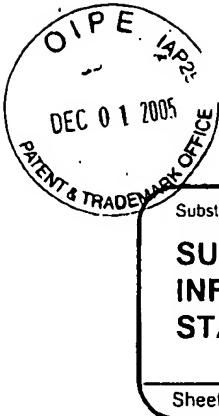
PTO/SB/08a (08-09)
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<p>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(Use as many sheets as necessary)</p>	
Sheet	1
at	1
<i>Complete if Known</i>	
Application Number	10/647,595
Filing Date	August 25, 2003
First Named Inventor	S. Brandon Keller
Art Unit	2409
Examiner Name	Not Yet Assigned
Attorney Docket Number	100111227-1

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	8/24/06
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PTO/SB/08a (08-03)

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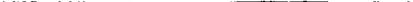
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		<i>Application Number</i>	10/647,595
		<i>Filing Date</i>	August 25, 2003
		<i>First Named Inventor</i>	S. Brandon Keller
		<i>Art Unit</i>	2825
		<i>Examiner Name</i>	Not Yet Assigned
<i>Sheet</i>	1	<i>of</i>	1
		<i>Attorney Docket Number</i>	100111227-1

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number + Kind Code ² (if known)			
		US-5,831,869	11-03-1998	Ellis, et al.	
		US-6,028,991	02-22-2000	Akashi	
		US-6,230,299	05-08-2001	McSherry, et al.	
		US-6,308,304	10-23-2001	Devgan, et al.	
		US-2002/002701	01-03-2002	Usami, et al.	
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		US-			
		US-			
		US-			

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Examiner Signature		Date Considered	8/24/06
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¹EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ²Applicant's unique citation designation number (optional). ³See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ⁴Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁵For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁶Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁷Applicant is to place a check mark here if English language Translation is attached.

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(Use as many sheets as necessary)

Sheet 1 of 1

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		Number - Kind Code ² (if known)	MM-DD-YYYY		
		US-2002/0144219	10-03-2002	Zachariah, et al.	
		US- 2003/0200519	10-23-2003	Argyres	
		US- 2003/0208721	11-06-2003	Regnier	
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		US- 5,682,320	10-28-1997	Khouja, et al.	
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		US- 6,751,782 B2	08-15-2004	Levin, et al.	
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		US- 6,836,877	12-28-2004	Dupenloup	

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**Examiner
Signature**

Ramkumar Path

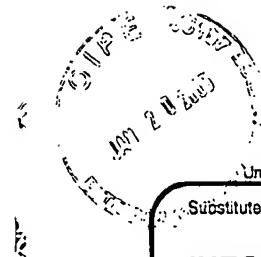
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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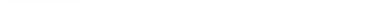
<i>Application Number</i>	10/647,595
<i>Filing Date</i>	August 25, 2003
<i>First Named Inventor</i>	S. Brandon Keller
<i>Art Unit</i>	
<i>Examiner Name</i>	

Attorney Docket Number

100111227-1

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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PATENT APPLICATION

Sheet 1 of 6

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT FEB 06 2004 (Use several sheets if necessary)		ATTY. DOCKET NO. 100111227-1	APPLICATION NO. 10/647,595	CONFIRMATION NO.
		APPLICANT S. Brandon Keller, et al.		
		FILING DATE Aug. 25, 2003	GROUP	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
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FOREIGN PATENT DOCUMENTS

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1L					
1M					
1N					
1O					
1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
1R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
1S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,376, Entitled: Method And Program For Visual Display and One-Click Repair Of Electromigration And Self Heating Design-Rule Violations In A Digital Integrated Circuit Layout Database; Filed November 12, 2003

EXAMINER

Brandon Dabbs

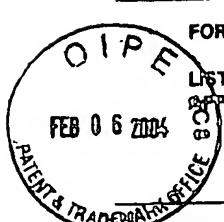
Rev 10/03 (PTO1449)

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PATENT APPLICATION

Sheet 2 of 6



FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
100111227-1	10/647,595	
APPLICANT		
S. Brandon Keller, et al.		
FILING DATE	GROUP	
Aug. 25, 2003		

REFERENCE DESIGNATION

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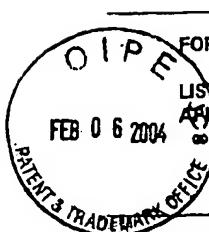
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

8	2Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
8	2R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,526, Entitled: Method And Program Product For Performing A Worst Case Electromigration And Self Heating Analysis In A Digital Integrated Circuit With Worst-Case Superposition Of Partial Currents; Filed November 12, 2003
8	2S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,692, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003

EXAMINER 	DATE CONSIDERED 8/24/06
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PATENT APPLICATION

Sheet 3 of 6



FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
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FEB 06 2004

(Use several sheets if necessary)

ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
100111227-1	10/647,595	
APPLICANT		
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FILING DATE	GROUP	
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3L					
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3O					
3P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

3Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,508, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Layout Database by Substituting Resistive Models For Active Devices; Filed November 12, 2003
3R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655516US; Entitled: Method And Program Product For Determining Worst Case Currents In A Digital Integrated Circuit Through Worst-Case Superposition Of Partial Currents; Docket No.: 200300123-1; Filed January 30, 2004
3S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655564US; Docket: 200308901-1; Entitled: Systems And Methods For Re-Using Circuit Design Analysis Results; Filed January 30, 2004

EXAMINER

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8/28/06

PATENT APPLICATION

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FORM PTO-1449

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8	4Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655581US; Docket No.: 200308899-1; Entitled: System And Method For Determining Detail Of Analysis In A Circuit Design; Filed January 30, 2004
8	4R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655595US; Docket No.: 200308898-1; Entitled: Systems And Methods That Identify Equivalent Instantiation-Specific Configuration Information For Analysis Tools; Filed January 30, 2004
8	4S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H. & STEVENS, Scott Alan; U.S. Patent Application filed under EV210655520US; Entitled: System And Method To Limit Analyzed Current Flow In A Circuit Design; Filed January 30, 2004

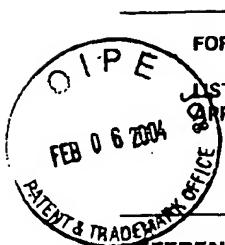
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PATENT APPLICATION

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81	5Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV21065553US; Docket: 200310172-1; Entitled: System And Method For Processing Configuration Information; Filed January 30, 2004
82	5R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655555US; Docket: 200310170-1; Entitled: System And Method For Balancing Run-Time And Result Accuracy In A Circuit Design Analysis Tool; Filed January 30, 2004
83	5S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655578US; Docket: 200308900-1; Entitled: System And Method For Indicating Logic State Combinations Used During Circuit Design Analysis; Filed January 30, 2004

EXAMINER

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PATENT APPLICATION

Sheet 6 of 6

O P E FEB 06 2004 U.S. PATENT & TRADEMARK OFFICE		ATTY. DOCKET NO. 100111227-1	APPLICATION NO. 10/647,595	CONFIRMATION NO.
FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANT S. Brandon Keller, et al.		
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6Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655547US; Docket: 200310171-1; Entitled: System And Method For Determining Control Signal Combinations For Use During Simulation Of A Stage Of A Circuit Design; Filed January 30, 2004
6R	
6S	

EXAMINER

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